MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE, MADANAPALLE
(UGC - AUTONOMOUS)
(Affiliated to JNTUA, Ananthapuramu & Approved by AICTE, New Delhi)
An ISO 9001: 2008 Certified Institution; www.mits.ac.in

Examination Section

Schedule - M.Tech – I Year I Semester (R14) Supplementary End Semester Examinations January – 2017

Academic Year – 2016 - 17   **TIMINGS: FN: 10:00AM to 01:00PM**

DATE / DAY	Electrical Power Systems (EPS)	Solar Power Systems (SPS)	Advanced Manufacturing Systems (AMS)	Digital Electronics & Communication Systems (DECS)	VLSI & Embedded Systems (VES)	Computer Science & Engineering (CSE)
17.01.2017 (Tuesday)	Modern Control Theory 14EPS11T01	Solar PV Technology 14SPS11T03	Automation in Manufacturing 14AMS11T01	Structural Digital Systems Design 14DECS11T01	VLSI Technology 14VES11T01	Programming Using Python 14CSP11T01
19.01.2017 (Thursday)	Power System Control & Stability 14EPS11T02	Modern Control Theory 14SPS11T04	Advanced Manufacturing Processes 14AMS11T02	Advanced Computer Networks 14DECS11T02	Embedded System Concepts 14VES11T02	Advanced Data structures and Algorithms 14CSP11T02
21.01.2017 (Saturday)	Operation and Control of Power System 14EPS11T03	Energy Storage in Solar Power System 14SPS11T05	Material Technology 14AMS11T03	Digital Communication Techniques 14DECS11T03	Structured Digital Systems Design 14VES11T03	Computer System Design 14CSP11T03
23.01.2017 (Monday)	Integration of Renewable Energy Sources 14EPS11T04	Basics of Solar Cell Devices 14SPS11T01	Product Design & Development 14AMS11T04	Advanced Digital Signal Processing 14DECS11T04	Analog & Digital IC Design 14VES11T04	Advanced Databases 14CSP11T04
25.01.2017 (Wednesday)	Analysis of Power Electronic Converters 14EPS11T05	Power Electronic Converters 14SPS11T02	Theory of Metal Cutting & Tool Design 14AMS11T05	Adaptive Signal Processing 14DECS11T05	Hardware Description Languages 14VES11T05	Object Oriented Analysis Design and Design Pattern 14CSP11T05
27.01.2017 (Friday)	Artificial Intelligence Techniques 14EPS11E1a	Artificial Intelligence Techniques 14SPS11E1c	Advanced Computer Aided Design 14AMS11E1b	Mobile Networks 14DECS11E1c	Mems 14VES11E1a	Software Engineering 14CSP11E1a

Note: (i) Any clashes or omissions in the time table may please be informed to the Controller of Examinations Immediately.

(ii) Even if Government declares holiday on any of the above dates, the examination shall be conducted as usual.

CONTROLLER OF EXAMINATIONS
Date: 15.12.2016
Copy to: P.A to Principal
      All the HODs for circulation among the staff members and display on notice board,
      Assistant Controllers of Examinations,
      Senior Administrative Officer,
      Senior Placement Officer,
      Transport Incharge

PRINCIPAL

Principal
Madanapalle Institute of
Technology & Science
MADANAPALLE